

## Recall: Flip-flop

A *flip-flop* is a 1-bit memory circuit.









**Sequential Circuits** 

## SR Flip-Flop

- The SR flip-flop actually has three inputs: S, R, and its current output, Q.
- Thus, we can construct a truth table for this circuit, as shown at the right.
- Notice the two undefined values. When both S and R are 1, the SR flipflop is unstable.

	P	resent State	Next State
s	R	Q(t)	Q(t+1)
0	0	0	0
0	0	1	1
0	1	0	0
0	1	1	0
1	0	0	1
1	0	1	1
1	1	0	undefined
1	1	1	undefined









